## FPF2108－FPF2110 <br> IntelliMAX ${ }^{\text {TM }}$ Advanced Load Management Products

## Features

－ 1.8 to 5.5 V Input Voltage Range
－Controlled Turn－On
－ 200 mA and 400 mA Current Limit Options
－Undervoltage Lockout
－Thermal Shutdown
－＜2uA Shutdown Current
－Fast Current limit Response Time
－3us to Moderate Over Currents
－20ns to Hard Shorts
－Reverse Current Blocking
－Fault Blanking

## Applications

－PDAs
－Cell Phones
－GPS Devices
－MP3 Players
－Digital Cameras
－Peripheral Ports
－Hot Swap Supplies

## General Description

The FPF2108－FPF2110 is a series of load switches which provides full protection to systems and loads which may encounter large current conditions．This devices contain a $0.125 \Omega$ current－limited P－channel MOSFET which can operate over an input voltage range of 1．8－5．5V．Internally，reverse current blocking prevents current from flowing when the MOSFET is off and the output voltage is higher than the input voltage．Switch control is by a logic input（ON）capable of interfacing directly with low voltage control signals．Each part contains thermal shutdown protection which shuts off the switch to prevent damage to the part when a continuous over－current condition causes excessive heating．
When the switch current reaches the current limit，the part operates in a constant－current mode to prohibit excessive currents from causing damage．For the FPF2108，if the constant current condition still persists after 10 ms ，the part will shut off the switch and pull the fault signal pin（FLAGB）low．The switch will remain off until the ON pin is cycled．For the FPF2109 and FPF2110，a current limit condition will immediately pull the fault signal pin low and the part will remain in the constant－current mode until the switch current falls below the current limit．The minimum current limit is 200 mA for the FPF2109 while that for the FPF2108 and FPF2110 is 400 mA ．

These parts are available in a space－saving 5 pin SOT23 package．

## Typical Application Circuit



## Functional Block Diagram



## Pin Configuration



## Pin Description

| Pin | Name | Function |
| :---: | :---: | :--- |
| 1 | VIN $^{\prime}$ | Supply Input: Input to the power switch and the supply voltage for the IC |
| 2 | GND | Ground |
| 3 | ON | ON Control Input |
| 4 | FLAGB | Fault Output: Active LO, open drain output which indicates an over current supply, <br> under voltage or over temperature state. |
| 5 | V OUT | Switch Output: Output of the power switch |

Absolute Maximum Ratings

| Parameter |  | Min. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {IN }}, \mathrm{V}_{\text {OUT }}, \mathrm{ON}, \mathrm{FLAGB}$ to GND |  | -0.3 | 6 | V |
| Power Dissipation @ $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ (note 1) |  |  | 667 | mW |
| Operating Temperature Range |  | -40 | 125 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature |  | -65 | 150 | ${ }^{\circ} \mathrm{C}$ |
| Thermal Resistance, Junction to Ambient |  |  | 150 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Electrostatic Discharge Protection | HBM | 4000 |  | V |
|  | MM | 400 |  | V |

## Recommended Operating Range

| Parameter | Min. | Max. | Unit |
| :--- | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{IN}}$ | 1.8 | 5.5 | V |
| Ambient Operating Temperature, $\mathrm{T}_{\mathrm{A}}$ | -40 | 85 | ${ }^{\circ} \mathrm{C}$ |

## Electrical Characteristics

$\mathrm{V}_{I N}=1.8$ to $5.5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=-40$ to $+85^{\circ} \mathrm{C}$ unless otherwise noted. Typical values are at $\mathrm{V}_{I N}=3.3 \mathrm{~V}$ and $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.

| Parameter | Symbol | Conditions |  | Min. | Typ. | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Basic Operation |  |  |  |  |  |  |  |
| Operating Voltage | $\mathrm{V}_{\text {IN }}$ |  |  | 1.8 |  | 5.5 | V |
| Quiescent Current | $\mathrm{I}_{\mathrm{Q}}$ | $\mathrm{I}_{\text {OUT }}=0 \mathrm{~mA}$ <br> Von active | $\mathrm{V}_{1 \mathrm{~N}}=1.8$ to 3.3 V |  | 95 |  | $\mu \mathrm{A}$ |
|  |  |  | $\mathrm{V}_{\mathrm{IN}}=3.3$ to 5.5 V |  | 110 | 200 |  |
| Shutdown Current | ISHDN |  |  |  |  | 2 | $\mu \mathrm{A}$ |
| Reverse Block Leakage Current | $\mathrm{I}_{\text {BLOCK }}$ |  |  |  |  | 1 | $\mu \mathrm{A}$ |
| Latch-Off Current (note 2) | l Latchoff | $\mathrm{V}_{\mathrm{ON}}=\mathrm{V}_{\text {IN }}$, after an overcurrent fault |  |  | 50 |  | $\mu \mathrm{A}$ |
| On-Resistance | $\mathrm{R}_{\text {ON }}$ | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{I}_{\text {OUT }}=50 \mathrm{~mA}$ |  |  | 125 | 160 | $\mathrm{m} \Omega$ |
|  |  | $\mathrm{T}_{\text {A }}=-40$ to $+85^{\circ} \mathrm{C}$, $\mathrm{l}_{\text {OUT }}=50 \mathrm{~mA}$ |  |  | 150 |  |  |
| ON Input Logic High Voltage | $\mathrm{V}_{\mathrm{IH}}$ | $\mathrm{V}_{\text {IN }}=1.8 \mathrm{~V}$ |  | 0.75 |  |  | V |
|  |  | $\mathrm{V}_{\mathrm{IN}}=5.5 \mathrm{~V}$ |  | 1.30 |  |  |  |
| ON Input Logic Low Voltage | $\mathrm{V}_{\mathrm{IL}}$ | $\mathrm{V}_{\mathrm{IN}}=1.8 \mathrm{~V}$ |  |  |  | 0.5 | V |
|  |  | $\mathrm{V}_{\text {IN }}=5.5 \mathrm{~V}$ |  |  |  | 1.0 |  |
| ON Input Leakage |  | $\mathrm{V}_{\text {ON }}=\mathrm{V}_{\text {IN }}$ or GND |  |  |  | 1 | $\mu \mathrm{A}$ |
| Off Switch Leakage | ISWOFF | $\mathrm{V}_{\text {ON }}=0 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=0 \mathrm{~V}$ |  |  |  | 1 | $\mu \mathrm{A}$ |
| FLAGB Output Logic Low Voltage |  | $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{I}_{\text {SINK }}=10 \mathrm{~mA}$ |  |  | 0.1 | 0.2 | V |
|  |  | $\mathrm{V}_{\text {IN }}=1.8 \mathrm{~V}, \mathrm{I}_{\text {SINK }}=10 \mathrm{~mA}$ |  |  | 0.15 | 0.3 |  |
| FLAGB Output High Leakage Current |  | $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}$, Switch on |  |  |  | 1 | $\mu \mathrm{A}$ |
| Protections |  |  |  |  |  |  |  |
| Current Limit | ILIM | $\begin{aligned} & \mathrm{V}_{\text {IN }}=3.3 \mathrm{~V}, \\ & \mathrm{~V}_{\text {OUT }}=3.0 \mathrm{~V} \end{aligned}$ | FPF2109 | 200 | 300 | 400 | mA |
|  |  |  | FPF2108, FPF2110 | 400 | 600 | 800 |  |
| Thermal Shutdown |  | Shutdown Threshold |  |  | 140 |  | ${ }^{\circ} \mathrm{C}$ |
|  |  | Return from Shutdown Hysteresis |  |  | 130 |  |  |
|  |  |  |  |  | 10 |  |  |
| Under Voltage Shutdown | UVLO | $\mathrm{V}_{\text {IN }}$ Increasing |  | 1.5 | 1.6 | 1.7 | V |
| Under Voltage Shutdown Hysteresis |  |  |  |  | 47 |  | mV |

## Electrical Characteristics Cont.

$\mathrm{V}_{\text {IN }}=1.8$ to $5.5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=-40$ to $+85^{\circ} \mathrm{C}$ unless otherwise noted. Typical values are at $\mathrm{V}_{I N}=3.3 \mathrm{~V}$ and $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.

| Parameter | Symbol | Conditions | Min. | Typ. | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Dynamic |  |  |  |  |  |  |
| Turn on time | $\mathrm{t}_{\mathrm{ON}}$ | RL=500, $\mathrm{CL}=0.1 \mathrm{uF}$ |  | 25 |  | $\mu \mathrm{s}$ |
| Turn off time | $\mathrm{t}_{\text {OFF }}$ | RL=500, $\mathrm{CL}=0.1 \mathrm{uF}$ |  | 50 |  | $\mu \mathrm{s}$ |
| $\mathrm{V}_{\text {OUT }}$ Rise Time | $\mathrm{t}_{\mathrm{R}}$ | RL=500, , CL=0.1uF |  | 12 |  | $\mu \mathrm{s}$ |
| $\mathrm{V}_{\text {OUT }}$ Fall Time | $\mathrm{t}_{\mathrm{F}}$ | RL=500, CL=0.1uF |  | 136 |  | $\mu \mathrm{s}$ |
| Over Current Blanking Time | $\mathrm{t}_{\text {BLANK }}$ | FPF2108 | 5 | 10 | 20 | ms |
| Short Circuit Response Time |  | $\mathrm{V}_{\mathrm{IN}}=\mathrm{V}_{\mathrm{ON}}=3.3 \mathrm{~V}$. Moderate Over-Current Condition. |  | 3 |  | $\mu \mathrm{s}$ |
|  |  | $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\mathrm{ON}}=3.3 \mathrm{~V}$. Hard Short. |  | 20 |  | ns |

Note 1: Package power dissipation on 1square inch pad, 2 oz copper board.
Note 2: Applicable only to FPF2108. Latchoff current does not include current flowing into FLAGB.

## Typical Characteristics



Figure 1. Quiescent Current vs. Input Voltage


Figure 3. Ishutdown Current vs. Temperature


Figure 5. Reverse Current vs. VOUT


Figure 2. Quiescent Current vs. Temperature


Figure 4. $\mathrm{I}_{\text {Switch-OfF }}$ Current vs. Temperature


Figure 6. Reverse Current vs. Temperature

## Typical Characteristics



Figure 7. Current Limit vs. Output Voltage


Figure 9. $\mathrm{R}_{(\mathrm{ON})}$ vs. Temperature


Figure 10. $\mathrm{V}_{\mathrm{IH}}$ vs. $\mathrm{V}_{\mathrm{IN}}$


Figure 8. Current Limit vs. Temperature


Figure 11. $\mathrm{T}_{\mathrm{ON}} / T_{\text {Off }}$ vs. Temperature


Figure 12. $\mathrm{T}_{\text {RISE }} / T_{\text {FALL }}$ vs. Temperature

## Typical Characteristics



Figure 13. $\mathrm{T}_{\text {BLANK }}$ vs. Temperature

Figure 15. ToN Response


Figure 17. Short Circuit Response Time (Output Shorted to GND)


Figure 14. $\mathrm{T}_{\text {Blank }}$ Response
( $\mathrm{V}_{\mathrm{DRV}}$ signal forces the device to go into overcurrent condition.)


Figure 16. Toff Response


Figure 18. Current Limit Response (Switch power up to hard short)

## Typical Characteristics



Figure 19. Current Limit Response Time (Output Shorted to GND by 10 2 , moderate short)

## Description of Operation

The FPF2108-FPF2110 is a current limited switch that protects systems and loads which can be damaged or disrupted by the application of high currents. The core of the device is a $0.125 \Omega$ P-channel MOSFET and a controller capable of functioning over a wide input operating range of $1.8-5.5 \mathrm{~V}$. The controller protects against system malfunctions through current limiting, under-voltage lockout and thermal shutdown. The current limit is preset for either 200 mA or 400 mA .

## On/OffControl

The ON pin controls the state of the switch. Active HI and LO versions are available. Refer to the Ordering Information for details. Activating ON continuously holds the switch in the on state so long as there is no fault. For all versions, an under-voltage on $\mathrm{V}_{\mathrm{IN}}$ or a junction temperature in excess of $150^{\circ} \mathrm{C}$ overrides the ON control to turn off the switch. For FPF2108, the ON pin must be toggled to turn on the switch again. The FPF2109 and FPF2110 do not turn off in response to a over current condition but instead remain operating in a constant current mode so long as ON is active and the thermal shutdown or under-voltage lockout have not activated. When the MOSFET is off, the body diode is disabled so no current can flow through it.

## Fault Reporting

Upon the detection of an over-current, an input under-voltage, or an over-temperature condition, the FLAGB signals the fault mode by activating LO. For the FPF2108, the FLAGB goes LO at the end of the blanking time while FLAGB goes LO immediately for the FPF2109 and FPF2110. For the FPF2108, FLAGB is latched LO and ON must be toggled to release it.With the FPF2109 and FPF2110, FLAGB is LO during the faults and immediately returns HI at the end of the fault condition. FLAGB is an open-drain MOSFET which requires a pull-up resistor between $\mathrm{V}_{I N}$ and FLAGB. During shutdown, the pull-down on FLAGB is disabled to reduce current draw from the supply.

## Current Limiting

The current limit ensures that the current through the switch doesn't exceed a maximum value while not limiting at less than a minimum value. For the FPF2109 the minimum current is 200 mA and the maximum current is 400 mA and for the FPF2108 and FPF2110 the minimum current is 400 mA and the maximum current is 800 mA . The FPF2108 has a blanking time of 10 ms , nominally, during which the switch will act as a constant current source. At the end of the blanking time, the switch will be turned-off and the FLAGB pin will activate to indicate that current limiting has occurred. The FPF2109 and FPF2110 have no current limit blanking period so immediately upon a current
limit condition FLAGB is activated. These parts will remain in a constant current state until the ON pin is deactivated or the thermal shutdown turns-off the switch.

## Under-Voltage Lockout

The under-voltage lockout turns-off the switch if the input voltage drops below the under-voltage lockout threshold. With the ON pin active the input voltage rising above the under-voltage lockout threshold will cause a controlled turn on of the switch which limits current over-shoots.

## Thermal Shutdown

The thermal shutdown protects the part from internally or externally generated excessive temperatures. During an over-temperature condition the FLAGB is activated and the switch is turned-off. The switch automatically turns-on again if the temperature of the die drops below the threshold temperature.

## Ordering Information

| Part | Current Limit <br> [mA] | Current Limit <br> Blanking Time <br> [ms] | Auto-Restart <br> Time <br> [ms] | ON Pin <br> Activity | Top Mark |
| :---: | :---: | :---: | :---: | :---: | :---: |
| FPF2108 | 400 | 10 | NA | Active LO | 2108 |
| FPF2109 | 200 | 0 | NA | Active HI | 2109 |
| FPF2110 | 400 | 0 | NA | Active HI | 2110 |

## Application Information

## Typical Application



## Input Capacitor

To limit the voltage drop on the input supply caused by transient in-rush currents when the switch turns-on into a discharged load capacitor or a short-circuit, a capacitor needs to be placed between $\mathrm{V}_{I N}$ and GND. A $0.1 u F$ ceramic capacitor, $\mathrm{C}_{\mathrm{IN}}$, placed close to the pins is usually sufficient. Higher values of $\mathrm{C}_{\mathrm{IN}}$ can be used to further reduce the voltage drop.

## Output Capacitor

A 0.1 uF capacitor $\mathrm{C}_{\text {OUT }}$, should be placed between $\mathrm{V}_{\text {OUT }}$ and GND. This capacitor will prevent parasitic board inductances from forcing $\mathrm{V}_{\text {OUT }}$ below $G N D$ when the switch turns-off.

## Power Dissipation

During normal operation as a switch, the power dissipation is small and has little effect on the operating temperature of the part. The parts with the higher current limits will dissipate the most power and that will only typically be,

$$
P=\left(\mathrm{l}_{\mathrm{LIM}}\right)^{2} * \mathrm{R}_{\mathrm{DS}}=(0.8)^{2} * 0.125=80 \mathrm{~mW}
$$

When in current limit the maximum power dissipation will occur when the output is shorted to ground. A short on the output will cause the part to operate in a constant current state until the thermal shutdown activates. It will then cycle in and out of thermal shutdown so long as the ON pin is active and the short is present.

## Board Layout

For best performance, all traces should be as short as possible. To be most effective, the input and output capacitors should be placed close to the device to minimize the effects that parasitic trace inductances may have on normal and short-circuit operation. Using wide traces for $\mathrm{V}_{\mathbb{N}}, \mathrm{V}_{\text {OUT }}$ and $G N D$ will help minimize parasitic electrical effects along with minimizing the case to ambient thermal impedance.

## Dimensional Outline and Pad Layout

Package MA05B


LAND PATTERN RECOMMENDATION


NOTES: UNLESS OTHERWISE SPECIFIED
A) THIS PACKAGE CONFORMS TO JEDEC MATED, IANUE BY 199 . DATE JANUARY 1999
B) ALL DIMENSIONS ARE IN MILLIMETERS.

DETAIL A
5-Lead SOT23, JEDEC MO-178, 1.6mm
Package Number MA05B
MAO5BRevC

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| :---: | :---: | :---: | :---: | :---: |
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| CoolFET ${ }^{\text {m }}$ | FRFET ${ }^{\text {m }}$ | MICROCOUPLER ${ }^{\text {TM }}$ | PowerSaver ${ }^{\text {TM }}$ | SuperSOT ${ }^{\text {TM }}$-3 |
| CROSSVOLT ${ }^{\text {m }}$ | GlobalOptoisolator ${ }^{\text {TM }}$ | MicroFET ${ }^{\text {m }}$ | PowerTrench ${ }^{\circledR}$ | SuperSOT ${ }^{\text {TM }}$-6 |
| DOME ${ }^{\text {TM }}$ | $\mathrm{GTO}^{\text {m }}$ | MicroPak ${ }^{\text {™ }}$ | QFET ${ }^{\text {® }}$ | SuperSOT ${ }^{\text {TM }}$-8 |
| EcoSPARK ${ }^{\text {™ }}$ | $\mathrm{HiSeC}^{\text {тM }}$ | MICROWIRE ${ }^{\text {TM }}$ | QS ${ }^{\text {TM }}$ | SyncFET ${ }^{\text {TM }}$ |
| $\mathrm{E}^{2} \mathrm{CMOS}^{\text {m }}$ | $1^{2} \mathrm{C}^{\text {TM }}$ | MSX ${ }^{\text {m }}$ | QT Optoelectronics ${ }^{\text {TM }}$ | TinyLogic ${ }^{(1)}$ |
| EnSigna ${ }^{\text {TM }}$ | $i-L O^{\text {TM }}$ | MSXPro ${ }^{\text {TM }}$ | Quiet Series ${ }^{\text {TM }}$ | TINYOPTO ${ }^{\text {TM }}$ |
| FACT ${ }^{\text {TM }}$ | ImpliedDisconnect ${ }^{\text {TM }}$ | OCX ${ }^{\text {¹ }}$ | RapidConfigure ${ }^{\text {TM }}$ | TruTranslation ${ }^{\text {TM }}$ |
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